

BIBLIOGRAFÍA (LIBROS Y ARTÍCULOS)

- [1] Reduced-complexity decoding of ldpc codes. *IEEE Transactions on Communications*, 53(7):1232–1232, July 2005.
- [2] K. H. Abed and R. E. Siferd. Cmos vlsi implementation of a low-power logarithmic converter. *IEEE Transactions on Computers*, 52(11):1421–1433, November 2003.
- [3] D. Abematsu, T. Ohtsuki, S. P. W. Jarot, and T. Kashima. Size compatible (SC)-array LDPC codes. In *Vehicular Technology Conference, 2007. VTC-2007 Fall. 2007 IEEE 66th*, pages 1147–1151, September/October 2007.
- [4] A. Anastasopoulos. A comparison between the sum-product and the min-sum iterative detection algorithms based on density evolution. In *Global Telecommunications Conference, 2001. GLOBECOM '01. IEEE*, volume 2, pages 1021–1025, San Antonio, TX, November 25–29, 2001.
- [5] A. Anastasopoulos. A comparison between the sum-product and the min-sum iterativedetection algorithms based on density evolution. In *Global Telecommunications Conference, 2001. GLOBECOM '01. IEEE*, volume 2, pages 1021–1025, San Antonio, TX, USA, 2001.
- [6] L. Bahl, J. Cocke, F. Jelinek, and J. Raviv. Optimal decoding of linear codes for minimizing symbol error rate (corresp.). *IEEE Transactions on Information Theory*, 20(2):284–287, March 1974.
- [7] Leonard E. Baum and Ted Petrie. Statistical inference for probabilistic functions of finite state Markov chains. *Ann. Math. Statist.*, 37:1554–1563, 1966.
- [8] Jinghu Chen and M. P. C. Fossorier. Decoding low-density parity check codes with normalized app-basedalgorithm. In *Global Telecommunications Conference, 2001. GLOBECOM '01. IEEE*, volume 2, pages 1026–1030, San Antonio, TX, USA, 2001.
- [9] Jinghu Chen and P. M. C. Fossorier. Density evolution for BP-based decoding algorithms of LDPC codes and their quantized versions. In *Global Telecommunications Conference, 2002. GLOBECOM '02. IEEE*, volume 2, pages 1378–1382, November 2002.
- [10] E. Eleftheriou, T. Mittelholzer, and A. Dholakia. Reduced-complexity decoding algorithm for low-density parity-check codes. *Electronics Letters*, 37:102–104, January 18, 2001.

- [11] J. Erfanian, S. Pasupathy, and G. Gulak. Reduced complexity symbol detectors with parallel structure for isi channels. *IEEE Transactions on Communications*, 42:1661–1671, 1994.
- [12] M. Franceschini, G. Ferrari, and R. Raheli. Does the performance of ldpc codes depend on the channel? *IEEE Transactions on Communications*, 54(12):2129–2132, December 2006.
- [13] R. Gallager. Low-density parity-check codes. *IRE Transactions on Information Theory*, 8(1):21–28, January 1962.
- [14] J. Hagenauer, E. Offer, and L. Papke. Iterative decoding of binary block and convolutional codes. *IEEE Transactions on Information Theory*, 42(2):429–445, March 1996.
- [15] Zhiyong He, Sebastien Roy, and Paul Fortier. Fpga implementation of ldpc decoders based on joint row-column decoding algorithm. In *Circuits and Systems, 2007. ISCAS 2007. IEEE International Symposium on*, pages 1653–1656, New Orleans, LA., May 27–30, 2007.
- [16] Hyoung Soon Kim and Sin Chong Park. Iteration based performance evaluation of ldpc coded mimo-ofdm. In *Advanced Communication Technology, 2006. ICACT 2006. The 8th International Conference*, volume 3, February 20–22, 2006.
- [17] Mong-Kai Ku, Huan-Sheng Li, and Yi-Hsing Chien. Code design and decoder implementation of low density parity check code. In *Emerging Information Technology Conference, 2005.*, August 15–16, 2005.
- [18] J. Li and X. D. Zhang. Reduced-complexity belief propagation decoding for low-density parity-check codes. *Electronics Letters*, 44(3):220–222, January 31, 2008.
- [19] Michael G. Luby, Michael Mitzenmacher, M. Amin, Shokrollahi Daniel, and A. Spielman. Analysis of low density codes and improved designs using irregular graphs. In *In Proc. of ACM STOC*, pages 249–258, 1998.
- [20] D. J. C. MacKay. Good error-correcting codes based on very sparse matrices. In *Information Theory. 1997. Proceedings., 1997 IEEE International Symposium on*, Ulm, Germany, June 29–July 4, 1997.
- [21] J.N. Mitchell Jr. Computer multiplication and division using binary logarithms. In *Electronic Computers, 1962. IRE Trans*, volume 11, pages 512–517, August 1962.
- [22] Todd K. Moon. *Error Correction Coding: Mathematical Methods and Algorithms*. Wiley-Interscience, 2005.
- [23] S. Papaharalabos, M. Papaleo, P. T. Mathiopoulos, M. Neri, A. Vanelli-Coralli, and G. E. Corazza. Dvb-s2 ldpc decoding using robust check node update approximations. *IEEE Transactions on Broadcasting*, 54(1):120–126, March 2008.
- [24] S. Papaharalabos, P. Sweeney, B. G. Evans, G. Albertazzi, A. Vanelli-Coralli, and G. E. Corazza. Performance evaluation of a modified sum-product decoding algo-

- rithm for ldpc codes. In *Wireless Communication Systems, 2005. 2nd International Symposium on*, pages 800–804, September 5–7, 2005.
- [25] S. Papaharalabos, P. Sweeney, B. G. Evans, P. T. Mathiopoulos, G. Albertazzi, A. Vanelli-Coralli, and G. E. Corazza. Modified sum-product algorithms for decoding low-density parity-check codes. *IET Communications*, 1(3):294–300, June 2007.
 - [26] Li Ping and W. K. Leung. Decoding low density parity check codes with finite quantization bits. *IEEE Communications Letters*, 4(2):62–64, February 2000.
 - [27] Xie Renhong, Sun Jintao, Guo Shanhong, and Rui Yibin. An improved sova algorithm and dsp implementation for turbo codes decoding. In *Signal Processing, 2004. Proceedings. ICSP '04. 2004 7th International Conference on*, volume 3, pages 1873–1877, August 31–September 4, 2004.
 - [28] P. Robertson, E. Villebrun, and P. Hoeher. A comparison of optimal and sub-optimal map decoding algorithmsoperating in the log domain. In *Communications, 1995. ICC '95 Seattle, 'Gateway to Globalization', 1995 IEEE International Conference on*, volume 2, pages 1009–1013, Seattle, WA, USA, June 18–22, 1995.
 - [29] P. Robertson, E. Villebrun, and P. Hoeher. A comparison of optimal and sub-optimal map decoding algorithmsoperating in the log domain. In *Communications, 1995. ICC '95 Seattle, 'Gateway to Globalization', 1995 IEEE International Conference on*, volume 2, pages 1009–1013, Seattle, WA, USA, June 18–22, 1995.
 - [30] Manyuan Shen, Huaning Niu, Hui Liu, and J. A. Ritcey. Finite precision implementation of ldpc coded m-ary modulation over wireless channels. In *Signals, Systems and Computers, 2003. Conference Record of the Thirty-Seventh Asilomar Conference on*, volume 1, pages 114–118, November 9–12, 2003.
 - [31] T. Theodorides, G. Link, E. Swankoski, N. Vijaykrishnan, M. J. Irwin, and H. Schmit. Evaluating alternative implementations for ldpc decoder check node function. In *VLSI, 2004. Proceedings. IEEE Computer society Annual Symposium on*, pages 77–82, February 19–20, 2004.
 - [32] X. Wei and A.Ñ. Akansu. Density evolution for low-density parity-check codes undermax-log-map decoding. *Electronics Letters*, 37(18):1125–1126, August 30, 2001.